

**PATENT APPLICATION**  
**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of

Hisao KOYANAGI

Appln. No.: Unknown

Group Art Unit: Unknown

Confirmation No.: Unknown

Examiner: Unknown

Filed: July 10, 2001

For: VECTOR SCATTER INSTRUCTION CONTROL CIRCUIT AND VECTOR  
ARCHITECTURE INFORMATION PROCESSING EQUIPMENT

**PRELIMINARY AMENDMENT**

Commissioner for Patents  
Washington, D.C. 20231

Sir:

Prior to examination, kindly amend the above-identified application as follows:

**IN THE SPECIFICATION:**

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**(Amended)** Figs. 6(a) and 6(b) is a diagram showing a processing of VSC instruction and following LDS instruction.

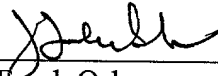
**(Amended)** Figs. 8(a) and 8(b) is a diagram showing conventional VGT instruction and VSC instruction.

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**REMARKS**

The specification has been amended to include a description of the figures shown in the formal drawings. Entry and consideration of this Amendment is respectfully requested.

Respectfully submitted,



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Date: July 10, 2001

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**APPENDIX**

**VERSION WITH MARKINGS TO SHOW CHANGES MADE**

**IN THE SPECIFICATION:**

~~Fig. 6~~ Figs. 6(a) and 6(b) is a diagram showing a processing of VSC instruction and following LDS instruction.

~~Fig. 8~~ Figs. 8(a) and 8(b) is a diagram showing conventional VGT instruction and VSC instruction.